

FAST - (10728304.wsp.1)

FileViewEditToolsWindowHelp

Drafts

BRS: 2

Pending

Active

Failed

Saved

L1: (16099) (PCB or printed adj circuit) with r

L2: (19575) test\$4 near5 (electric\$4 near4 comp

L3: (116) 1 and 2

L9: (2) ("5,698,895").PN.

S1: (800) 324/755.ccls.

S2: (451) S1 and socket

S3: (1473299) socket or carrier

S4: (19575) test\$4 near5 (electric\$4 near4 comp

S5: (4824) S3 and S4

S6: (195) S2 and S5

S7: (594) solder adj mask adj layer

S8: (0) S6 and S7

S9: (24837) solder near4 layer

S10: (15) S6 and S9

S11: (8325) 324/158.1,765.ccls.

S12: (1400) S4 and S11

S13: (27) S9 and S12

S14: (16099) (PCB or printed adj circuit) with

S15: (19575) test\$4 near5 (electric\$4 near4 com

S16: (116) S14 and S15

Search

DB: US-PG-PUB:USPAT:EPO:JPO:DERVENT:BM\_TOB

Default operator: CR

5,698,895

Highlight all hit terms initially

BRS form

IS&R form

Image

Text

HTML

	U	1	Document ID	Issue Dat	Pages	Title	Current OR	Current XR	Retrieval	Inventor	S	C	P	2	3	4	5
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5698895	19971216	17	Silicon segment programming method and	257/665	257/723;		Pedersen; David V. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5698895	19971216	17	Silicon segment stack addressing and programm		257/724;		FINLEY, M G et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>

Hits

Details

HTML

Ready

NUM